UTILITY PATENT APPLICATION TRANSMITTAL

(New Nonprovisional Applications Under 37 CFR § 1.53(b))

Attorney Docket No. YOR920030358US1

17302 U.S.PTO 10/720464

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Transmitted herewith is the patent application of () application identifier or (X) first named inventor, <u>Anthony Correale</u>, <u>Ir.</u>, entitled <u>Multiple Voltage Integrated Circuit and Design Method Therefor</u>, for a(n):

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Attorney Docket No.: YOR920030358US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re PATENT APPLICATION of:

Anthony Correale, Jr. et al.

Appln. No.:

Unknown

Art Unit:

Filed:

herewith

Examiner: Unknown

For:

MULTIPLE VOLTAGE INTEGRATED CIRCUIT AND DESIGN METHOD

THEREFOR

SUBMISSION OF FORMAL DRAWINGS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir,

Enclosed are ten (10) sheets of formal drawings including Figures 1A – 12 for the above-identified application.

Respectfully Submitted,

November 24, 2003

(Date)

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